

VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR

Akhil K. Garlapati, et al.

026-0044

1/3

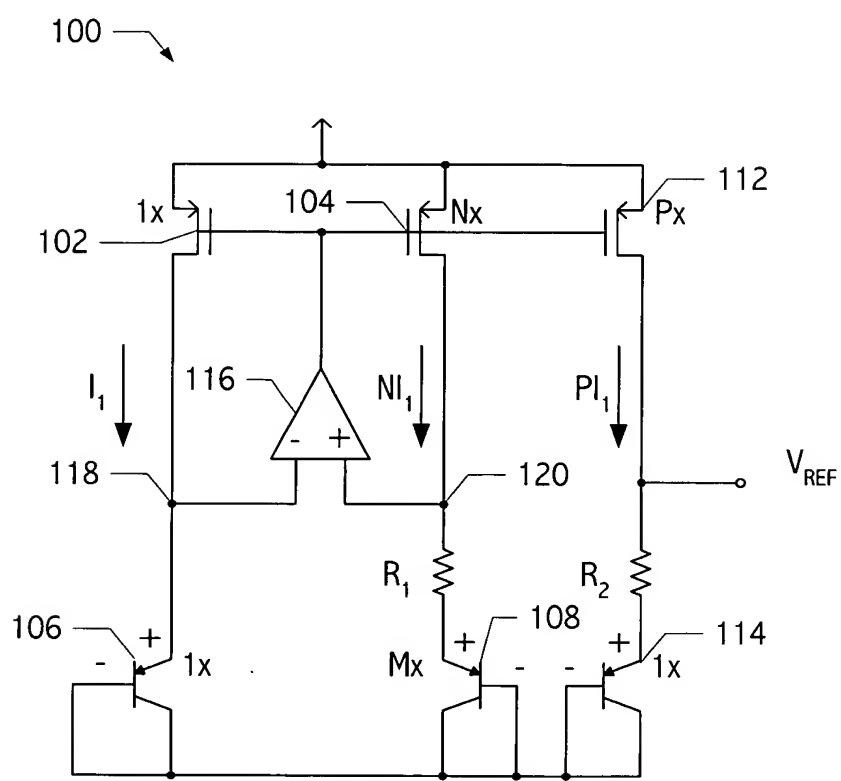


FIG. 1

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Akhil K. Garlapati, et al.

026-0044

2/3

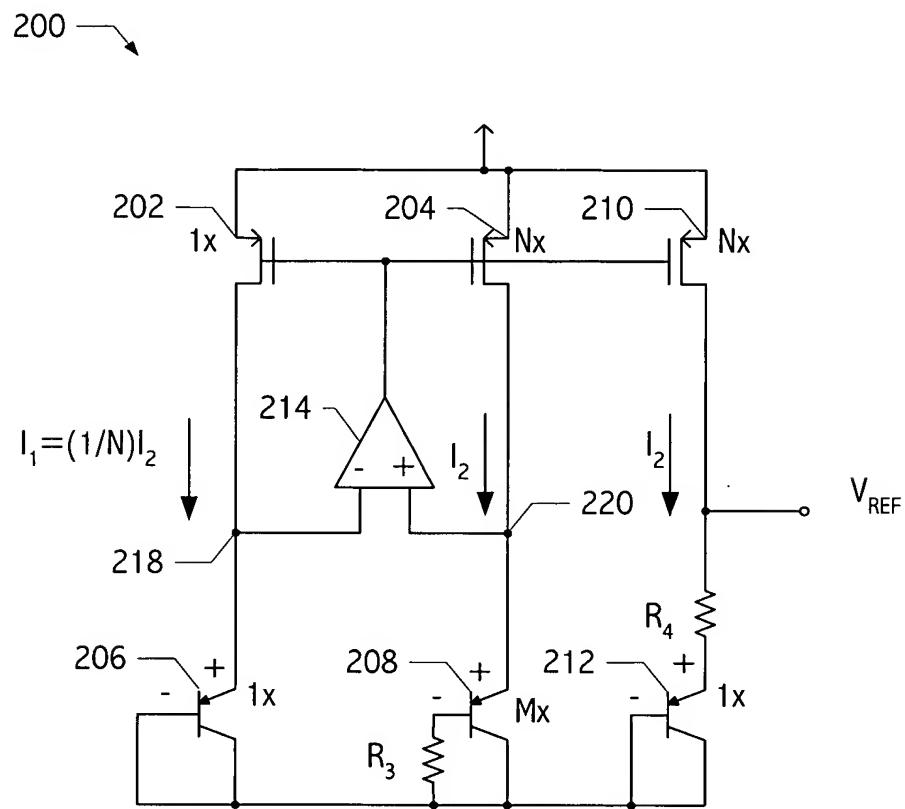


FIG. 2

VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR

Akhil K. Garlapati, et al.

026-0044

3/3

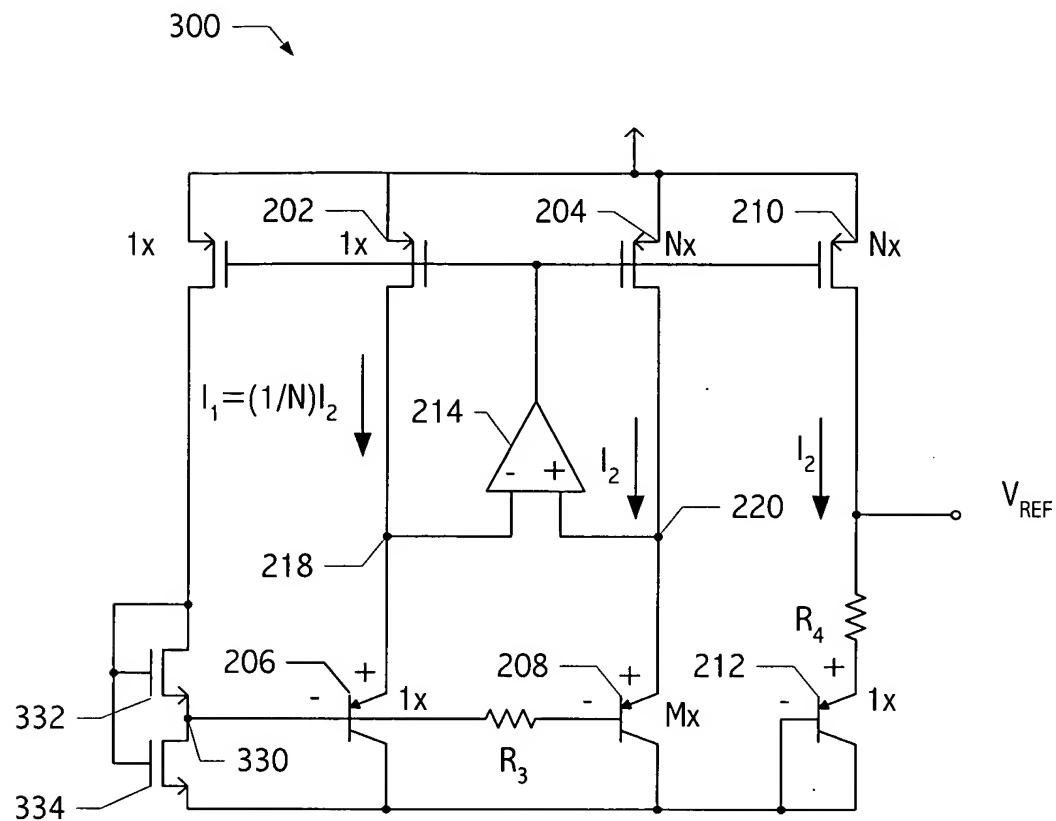


FIG. 3